

FIG. 1 (Prior Art)

100 ↗

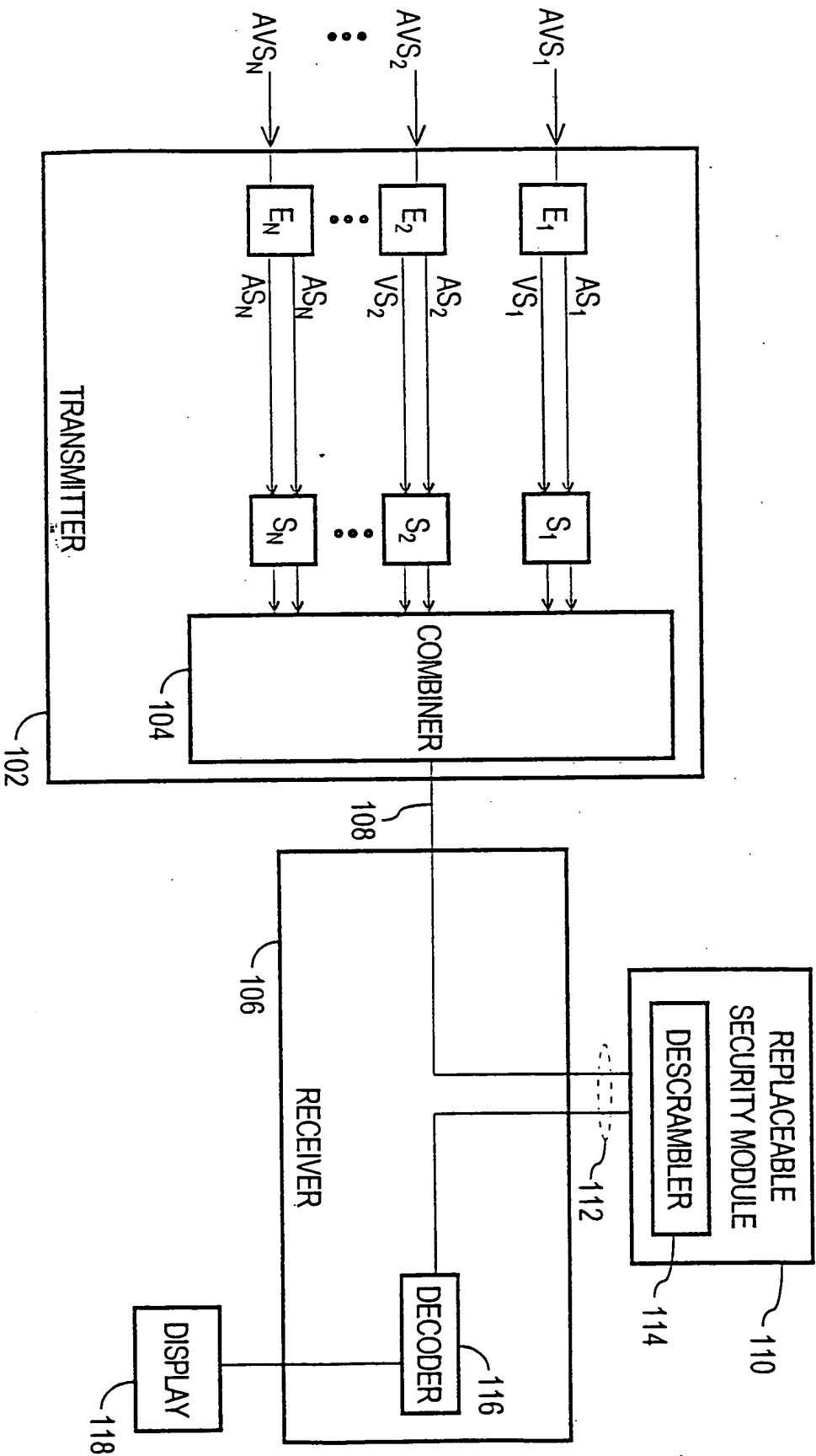
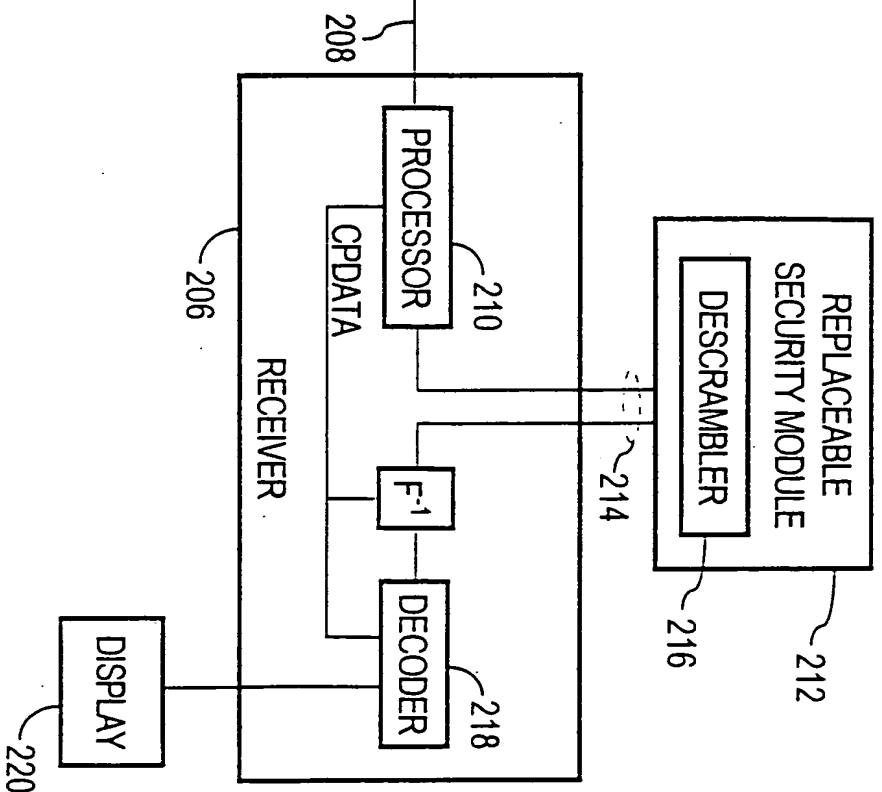
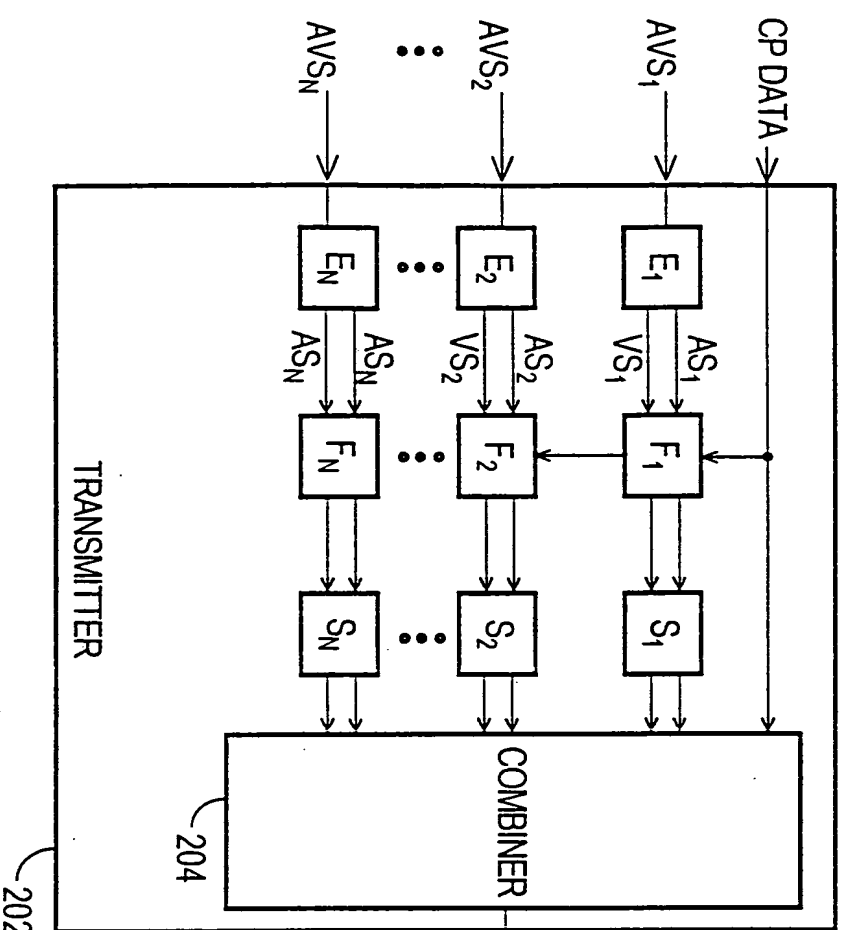


FIG. 2

200

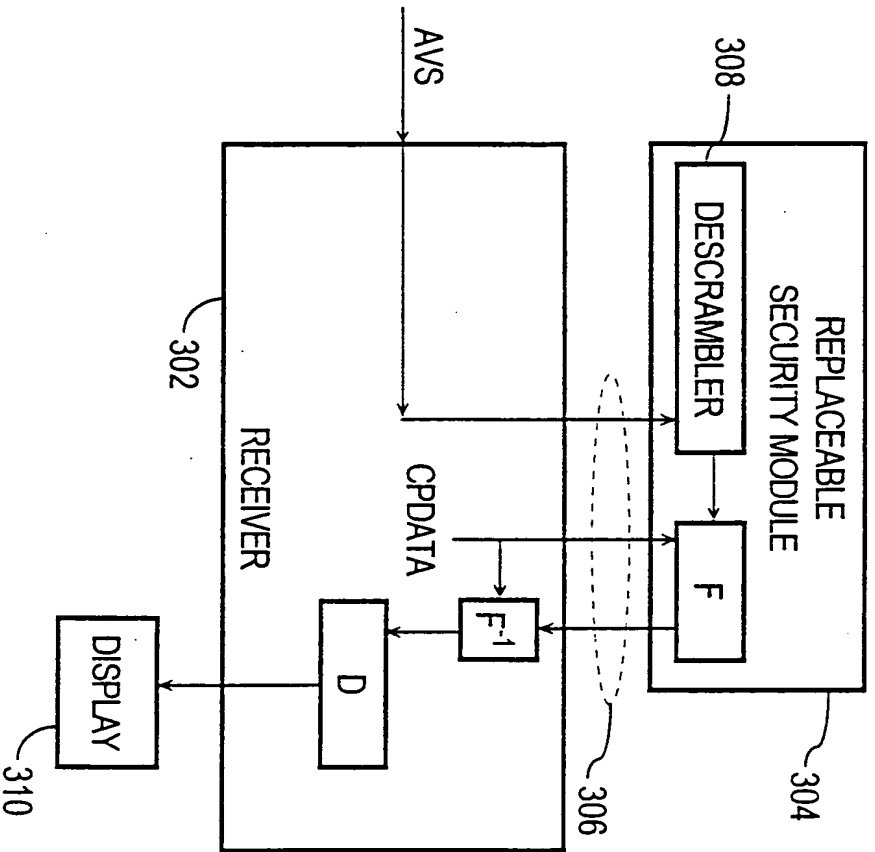


APPROVED	O.G. FIG.	CLASS	SUBCLASS
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FIG. 2 is a block diagram of a transmitter and receiver system. The transmitter (202) includes a combiner (204) that receives multiple parallel paths of data. Each path consists of an encoder (E<sub>i</sub>), a function block (F<sub>i</sub>), and a selector (S<sub>i</sub>). The receiver (206) includes a processor (210), a decoder (218), and a display (220). A replaceable security module (212) with a descrambler (216) is connected to the processor (210) via a bidirectional line (214). The system is labeled 200.

FIG. 3

300  
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FIG. 3 is a block diagram of a security system 300. The security system 300 includes a replaceable security module 304 and a receiver 302. The replaceable security module 304 includes a descrambler 308 and a functional block F. The receiver 302 includes a functional block F<sup>-1</sup> and a display 310. The receiver 302 receives an AVS signal and outputs CPDATA to the descrambler 308. The descrambler 308 outputs data to block F, which outputs data to block F<sup>-1</sup> in the receiver 302. Block F<sup>-1</sup> outputs data to the display 310. A dashed oval 306 encloses the descrambler 308 and block F.

FIG.4

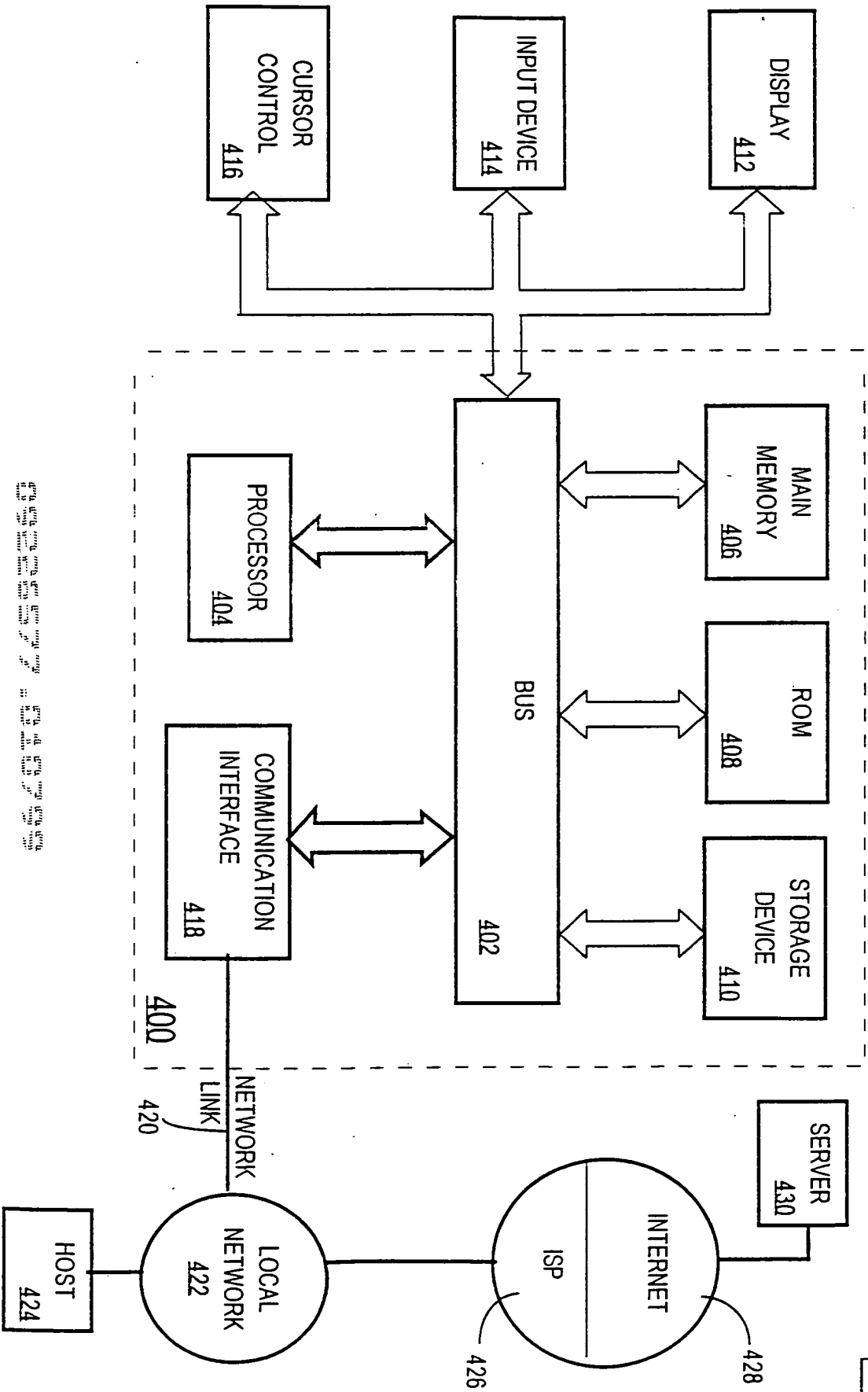


FIG. 4 is a block diagram of a computer system 400. The computer system 400 includes a central BUS 402. The BUS 402 is connected to MAIN MEMORY 406, ROM 408, STORAGE DEVICE 410, PROCESSOR 404, and COMMUNICATION INTERFACE 418. The COMMUNICATION INTERFACE 418 is connected to a NETWORK LINK 420. The NETWORK LINK 420 connects the computer system 400 to a LOCAL NETWORK 422 and the INTERNET 428. The LOCAL NETWORK 422 includes a HOST 424. The INTERNET 428 includes an ISP 426 and a SERVER 430.